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... GPERF: a perfect hash **function generator**. Source, More C++ gems table of contents.
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W Liu, SI Liu - ELECTRONICS LETTERS, 2003 - ieeexplore.ieee.org
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1 A VHDL error simulator for functional test generation   ■

Alessandro Fin, Franco Fummi
January 2000 **Proceedings of the conference on Design, automation and test in Europe**
Publisher: ACM Press
Full text available: ▤ pdf(72.38 KB)
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Additional Information: full citation, references, citings, index terms

2 Test program generation for functional verification of PowerPC processors in IBM   ■

Aharon Aharon, Dave Goodman, Moshe Levinger, Yossi Lichtenstein, Yossi Malka, Charlotte Metzger, Moshe Molcho, Gil Shurek
January 1995 **Proceedings of the 32nd ACM/IEEE conference on Design automation**
Publisher: ACM Press
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3 A functional approach to generation with TAG   ■

Kathleen F. McCoy, K. Vijay-Shanker, Gijoo Yang
June 1992 **Proceedings of the 30th annual meeting on Association for Computational Linguistics**
Publisher: Association for Computational Linguistics
Full text available: ▤ pdf(807.91 KB)
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It has been hypothesized that Tree Adjoining Grammar (TAG) is particularly well suited for sentence generation. It is unclear, however, how a sentence generation system based on TAG should choose among the syntactic possibilities made available in the grammar. In this paper we consider the question of what needs to be done to generate with TAGs and explain a generation system that provides the necessary features. This approach is compared with other TAG-based generation systems. Particular atten ...

4 General random number generator [G5]   ■

Edgar L. Butler
January 1970 **Communications of the ACM,** Volume 13 Issue 1

Publisher: ACM Press
Full text available: pdf(691.43 KB)   Additional Information: <u>full citation</u>, <u>references</u>, <u>citings</u>

Keywords: cumulative density function, probability density function, random number generator, transformation

5   ESF: an automatically generated encyclopedia of special functions
Ludovic Meunier, Bruno Salvy
August 2003 **Proceedings of the 2003 international symposium on Symbolic and algebraic computation**
Publisher: ACM Press

Full text available: pdf(274.46 KB)   Additional Information: <u>full citation</u>, <u>abstract</u>, <u>references</u>, <u>citings</u>, <u>index terms</u>

We present our on-going work on the automatic generation of an encyclopedia of special functions on the web, called *The Encyclopedia of Special Functions* (ESF) footnoteurlhttp://algo.inria.fr/esf. All mathematical formulæ in the ESF are computed, typeset and displayed without any human intervention. This is achieved by exploiting a collection of computer algebra algorithms in a systematic way, on top of a specially designed data structure for a class of special functions.

Keywords: D-finite functions, special functions

6   Functional test generation for behaviorally sequential models
F. Ferrandi, G. Ferrara, D. Sciuto, A. Fin, F. Fummi
March 2001 **Proceedings of the conference on Design, automation and test in Europe**
Publisher: IEEE Press
Full text available: pdf(145.75 KB)   Additional Information: <u>full citation</u>, <u>references</u>, <u>citings</u>, <u>index terms</u>

7   Generation of formatters for context-free languages
Mark van den Brand, Eelco Visser
January 1996 **ACM Transactions on Software Engineering and Methodology (TOSEM)**,
Volume 5 Issue 1
Publisher: ACM Press

Full text available: pdf(2.33 MB)   Additional Information: <u>full citation</u>, <u>abstract</u>, <u>references</u>, <u>citings</u>, <u>index terms</u>, <u>review</u>

Good documentation is important for the production of reusable and maintainable software. For the production of accurate documentation it is necessary that the original program text is not copied manually to obtain a typeset version. Apart from being tedious, this will invariably introduce errors. The production of tools that support the production of legible and accurate documentation is a software engineering challenge in itself. We present an algebraic approach to the generation of tools ...

Keywords: document preparation, program generators

8   Module generation of complex macros for logic-emulation applications
Wen-Jong Fang, Allen C.-H. Wu, Duan-Ping Chen
February 1997 **Proceedings of the 1997 ACM fifth international symposium on Field-programmable gate arrays**

Publisher: ACM Press
Full text available: pdf(1.48 MB)   Additional Information: <u>full citation</u>, <u>references</u>, <u>citings</u>, <u>index terms</u>

9 <u>Run-time code generation and modal-ML</u>

Philip Wickline, Peter Lee, Frank Pfenning
May 1998 **ACM SIGPLAN Notices , Proceedings of the ACM SIGPLAN 1998 conference on Programming language design and implementation PLDI '98**, Volume 33 Issue 5
Publisher: ACM Press

Full text available: pdf(1.66 MB)   Additional Information: <u>full citation</u>, <u>abstract</u>, <u>references</u>, <u>citings</u>, <u>index terms</u>

This paper presents a typed programming language and compiler for run-time code generation. The language, called ML', extends ML with modal operators in the style of the Mini-ML'e language of Davies and Pfenning. ML' allows programmers to use types to specify precisely the stages of computation in a program. The types also guide the compiler in generating target code that exploits the staging information through the use of run-time code generation. The target machine is current ...

10 <u>Generation of LR parsers by partial evaluation</u>

Michael Sperber, Peter Thiemann
March 2000 **ACM Transactions on Programming Languages and Systems (TOPLAS)**, Volume 22 Issue 2
Publisher: ACM Press

Full text available: pdf(411.54 KB)   Additional Information: <u>full citation</u>, <u>abstract</u>, <u>references</u>, <u>index terms</u>, <u>review</u>

The combination of modern programming languages and partial evaluation yields new approaches to old problems. In particular, the combination of functional programming and partial evaluation can turn a general parser into a parser generator. We use an inherently functional approach to implement general LR(k) parsers and specialize them with respect to the input grammars using offline partial evaluation. The functional specification of LR parsing yields a concise implementat ...

Keywords: LR parsing, continuations, functional programming, parser generation, partial evaluation

11 <u>Code generation and analysis for the functional verification of micro processors</u>

Anoosh Hosseini, Dimitrios Mavroidis, Pavlos Konas
June 1996 **Proceedings of the 33rd annual conference on Design automation**
Publisher: ACM Press
Full text available: pdf(87.31 KB)   Additional Information: <u>full citation</u>, <u>references</u>, <u>citings</u>, <u>index terms</u>

12 <u>Algorithm 829: Software for generation of classes of test functions with known local and global minima for global optimization</u>

Marco Gaviano, Dmitri E. Kvasov, Daniela Lera, Yaroslav D. Sergeyev
December 2003 **ACM Transactions on Mathematical Software (TOMS)**, Volume 29 Issue 4
Publisher: ACM Press
Full text available: pdf(100.42 KB)   Additional Information: <u>full citation</u>, <u>abstract</u>, <u>references</u>, <u>index terms</u>

A procedure for generating non-differentiable, continuously differentiable, and twice continuously differentiable classes of test functions for multiextremal multidimensional box-constrained global optimization is presented. Each test class consists of 100

functions. Test functions are generated by defining a convex quadratic function systematically distorted by polynomials in order to introduce local minima. To determine a class, the user defines the following parameters: (i) problem dimension, ...

Keywords: Global optimization, known local minima, test problems generation

13 <u>Pseudo-random generation from one-way functions</u>

R. Impagliazzo, L. A. Levin, M. Luby

February 1989 **Proceedings of the twenty-first annual ACM symposium on Theory of computing**

Publisher: ACM Press

Full text available: <u>pdf(1.41 MB)</u>   Additional Information: <u>full citation</u>, <u>abstract</u>, <u>references</u>, <u>citings</u>, <u>index terms</u>

We show that the existence of one-way functions is necessary and sufficient for the existence of pseudo-random generators in the following sense. Let f be an easily computable function such that when x is chosen randomly: (1) from $f(x)$ it is hard to recover an x1 with $f(x1) = f(x)$ by a small circuit, or; (2) f has small degeneracy and from & ...

14 <u>Using generative programming to visualise hypercode in complex and dynamic systems</u>

Katherine Mickan, Ron Morrison, Graham Kirby, Dharini Balasubramaniam, Evangelos Zirintsis

January 2004 **Proceedings of the 27th Australasian conference on Computer science - Volume 26 ACSC '04**

Publisher: Australian Computer Society, Inc.

Full text available: <u>pdf(524.92 KB)</u>   Additional Information: <u>full citation</u>, <u>abstract</u>, <u>references</u>, <u>citings</u>

The research presented here takes place in the context of the EC Funded ArchWare project which focuses on innovative architecture-centric languages, frameworks and tools for engineering evolvable software systems. Of particular interest are complex and dynamic systems characterised by the need to evolve to meet changing requirements without total shutdown or the loss of state information. The ArchWare approach uses the unique combination of a pi-calculus based architecture description language, ...

Keywords: generative programming, hypercode, structural reflection, system evolution

15 <u>Functional test generation for delay faults in combinational circuits</u>

Irith Pomeranz, Sudhakar M. Reddy

December 1995 **Proceedings of the 1995 IEEE/ACM international conference on Computer-aided design**

Publisher: IEEE Computer Society

Full text available: <u>pdf(89.73 KB)</u>   Additional Information: <u>full citation</u>, <u>abstract</u>, <u>references</u>, <u>citings</u>, <u>index terms</u>
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Abstract: We propose a functional fault model for delay faults in combinational circuits and describe a functional test generation procedure based on this model. The proposed method is most suitable when a gate-level description of the circuit-under-test, necessary for employing existing gate-level delay fault test generators, is not available. It is also suitable for generating tests in early design stages of a circuit, before a gate-level implementation is selected. It can also potentially be ...

Keywords: combinational circuits, delay faults, delays, fault simulated, functional fault model, functional test generation, gate-level realizations, logic CAD, logic testing, test

generation, test generators

16 On-chip communication and interface design: Service dependency graph: an efficient
model for hardware/software interfaces modeling and generation for SoC design
Adriano Sarmento, Lobna Kriaa, Arnaud Grasset, Mohamed-Wassim Youssef, Aimen
Bouchhima, Frederic Rousseau, Wander Cesario, Ahmed Amine Jerraya
September 2005 **Proceedings of the 3rd IEEE/ACM/IFIP international conference on
                  Hardware/software codesign and system synthesis CODES+ISSS '05**
Publisher: ACM Press
Full text available: 📄 pdf(191.59 KB)   Additional Information: <u>full citation</u>, <u>abstract</u>, <u>references</u>, <u>index terms</u>

> Complex systems-on-chip are designed by interconnecting pre-designed hardware (HW)
> and software (SW) components. During the design cycle, a global model of the SoC may
> be composed of HW and SW models at different abstraction levels. Designing HW/SW
> interfaces to interconnect SoC components is a source of design bottlenecks. This paper
> describes a service-based model enabling systematic design and co-simulation of HW/SW
> interfaces for SoC design. This model, called Service dependency graph (SDG) ...
>
> **Keywords**: hardware/software interfaces, interface design automation, service-based
> model, systems-on-chip

17 Using focus to generate complex and simple sentences
Marcia A. Derr, Kathleen R. McKeown
July 1984 **Proceedings of the 22nd annual meeting on Association for Computational
           Linguistics , Proceedings of the 10th international conference on
           Computational linguistics**
Publisher: Association for Computational Linguistics , Association for Computational Linguistics
Full text available: 📄 pdf(671.35 KB)
                     📑 Publisher Site   Additional Information: <u>full citation</u>, <u>abstract</u>, <u>references</u>, <u>citings</u>

> One problem for the generation of natural language text is determining when to use a
> sequence of simple sentences and when a single complex one is more appropriate. In this
> paper, we show how focus of attention is one factor that influences this decision and
> describe its implementation in a system that generates explanations for a student advisor
> expert system. The implementation uses tests on functional information such as focus of
> attention within the Prolog definite clause grammar formalism t ...

18 Key management, key exchange, & pseudo-random generation: A model and
architecture for pseudo-random generation with applications to /dev/random
Boaz Barak, Shai Halevi
November 2005 **Proceedings of the 12th ACM conference on Computer and
              communications security CCS '05**
Publisher: ACM Press
Full text available: 📄 pdf(235.10 KB)   Additional Information: <u>full citation</u>, <u>abstract</u>, <u>references</u>, <u>index terms</u>

> We present a formal model and a simple architecture for robust pseudorandom generation
> that ensures resilience in the face of an observer with partial knowledge/control of the
> generator's entropy source. Our model and architecture have the following
> properties:*Resilience*. The generator's output looks random to an observer with no
> knowledge of the internal state. This holds even if that observer has complete control over
> data that is used to refresh the internal state.*Forward security*
> **Keywords**: *dev/random, entropy, mixing functions, pseudo-randomness, smart-cards,
> true randomness*

19 <u>A new general derandomization method</u>

Alexander E. Andreev, Andrea E. F. Clementi, José D. P. Rolim
January 1998 **Journal of the ACM (JACM)**, Volume 45 Issue 1
Publisher: ACM Press

Full text available: <u>pdf(229.61 KB)</u>   Additional Information: <u>full citation</u>, <u>abstract</u>, <u>references</u>, <u>citings</u>, <u>index terms</u>

We show that quick hitting set generators can replace quick pseudorandom generators to derandomize any probabilistic two-sided error algorithms. Up to now quick hitting set generators have been known as the general and uniform derandomization method for probabilistic one-sided error algorithms, while quick pseudorandom generators as the generators as the general and uniform method to derand ...

Keywords: BPP, Boolean circuits, derandomization

20 <u>Generation as structure driven derivation</u>

Jürgen Wedekind
August 1988 **Proceedings of the 12th conference on Computational linguistics - Volume 2**
Publisher: Association for Computational Linguistics
Full text available: <u>pdf(724.16 KB)</u>   Additional Information: <u>full citation</u>, <u>abstract</u>, <u>references</u>, <u>citings</u>

This paper describes two algorithms which construct two different types of generators for lexical functional grammars (LFGs). The first type generates sentences from functional structures and the second from semantic structures. The latter works on the basis of oxtended LFGs, which contain a mapping from f-structures into semantic structures. Both algorithms can be used on all grammars within the respective class of LFG-grammars. Thus sentences can be generated from input structures by means of ...

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